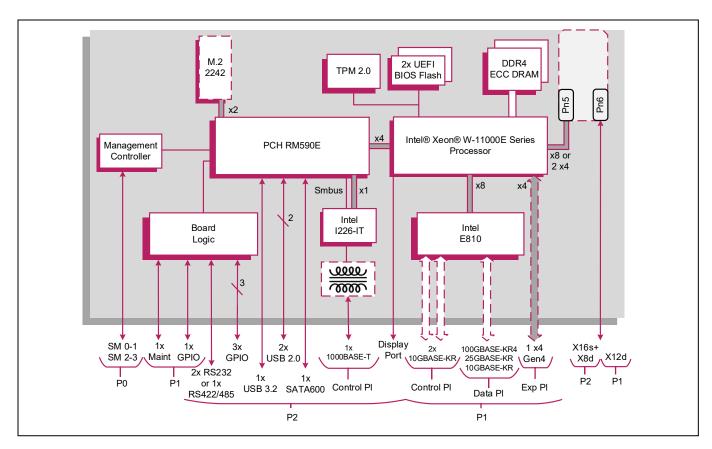
Rugged 3U VPX™ I/O Intensive Air Flow Thru Plug In Card (PIC)

Key Features

TR L98/9sd-AFT is a rugged 3U VPX[™] board based on the Intel[®] Xeon[®] W-11000E Series Processor with Air Flow Thru cooling for enhanced performance. It is designed in alignment with the SOSA[™] Technical Standard for I/O intensive processor boards.

- 8-core processor for high performance
- 100GBASE-KR4 Ethernet Data plane
- x4 Gen 4 PCI Express[®] Expansion plane for high-speed communication with adjacent board(s)
- XMC site for flexible I/O configuration
- Optional M.2 module for storage with Write/Protect and Opal 2.0 compliance for data at rest protection







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Specification

VPX Processor Board

- rugged air flow thru 3U VPX™ board utilizing processors based on Intel® Xeon® W-11000E Series Processor
- compliant with two OpenVPX[™] module profiles:
 - → SLT3-PAY-1F1F2U1TU1T1U1T-14.2.16
 - → MOD3-PAY-1F1F2U1TU1T1U1T-16.2.15-4

Central Processor

- 8-core Intel® Xeon® W-11865MRF Processor.
- Intel Advanced Vector Extensions AVX-512
- Intel Vector Neuro Network Instructions (VNNI)
- Intel® Iris® Xe (Gen 12) Graphics Engine with up to 32 EUs

DRAM

- 32 Gbytes soldered DDR4 ECC DRAM:
 - → ECC
 - single bit error correction
 - dual channel architecture

Optional XMC Site

- 1x XMC site, in a single VPX slot (VITA 42.0):
- → XMC rear I/O, providing X12d+x16s+X8d
- → 1 x8 or 2 x4 PCI Express® (PCle®)
- → PCle Gen 1, Gen 2 and Gen 3
- XMC connector type (build option):
 - → up to Gen 2, VITA 42 XMC (black color)
 - → up to Gen 3, VITA 61 XMC 2.0 (white color)
- XMC VPWR +12 V
- VITA 46.9 XMC I/O pin-out

Serial Ports

- 1x RS232/422/485 port accessed via P2
- 1x maintenance port accessed via P1
- Maintenance port on P1 supports RS232 or LVCMOS levels (build option)
- 16550 compatible UARTs

Graphics/Audio Interfaces

- 1x graphics/audio interface:
 - → DisplayPort v1.2 interface, supporting audio and video, via P2
 - → up to 3840 x 2160 @ 60 Hz, driver dependent

Other Peripheral Interfaces

- PC RTC, long duration timer, watchdog timer
- 2x USB 2.0 and 1x USB 3.2 (Gen 1) ports via
- 3x GPIO signals via P2
- 1x GPIO signal via P1

Mass Storage Interfaces

- 1x SATA 600 via P2
- 1x M.2 SSD site supports:
 - 2242 format module
 - → x2 PCle interface (M-key)
 - → Opal 2.0 security encryption
 - → Write Protect
 - → NVM Express® (NVMe®) logical device interface

VPX Control Plane. Ethernet

- configurable Control Plane (VITA 46.6)
- 1x 10/100/1000BASE-T Ethernet port via P2:
 - option for with or without magnetics
 - implemented by Intel Ethernet Controller 1225-IT
- up to 2x 10 Gigabit Ethernet ports via P1 (VITA) 46.7):
 - → supports up to 2x 10GBASE-KR
 - implemented by Intel Ethernet Controller E810 via x8 PCle
 - → factory build option available to disable Control Plane
- supports IEEE 1588 Precision Time Protocol

VPX Data Plane, 100 Gigabit Ethernet

- configurable Ethernet VPX Data Plane fabric interface (VITA 46.7)
- 1x 100 Gigabit Ethernet ports via P1 (VITA 46.7):
 - → supports 1x 100GBASE-KR4 or 1x 25GBASE-KR or 4x 10GBASE-KR
 - > implemented by Intel Ethernet Controller E810 via x8 PCle
 - → factory build option available to disable Data Plane
- supports IEEE 1588 Precision Time Protocol

VPX Expansion Plane, PCI Express

- configurable PCI Express (PCIe) VPX Expansion Plane fabric interface (VITA 46.4):
 - → 1 x4 Gen 4
 - → factory build option available to disable Expansion Plane
- PCIe interfaces support Gen 1, Gen 2, Gen 3 and Gen 4

Optional Built-In Test (BIT) Support

power-on BIT, Initiated BIT, Continuous BIT

System Management

- On board controller:
 - → SM0-1 and SM2-3
- VITA 46.11-2022 type 3 IPMC
- option for VITA 46.11-2022 compatible Tier 1 Chassis Manager

Board Security Packages

- Trusted Platform Module (TPM 2.0)
- supports Total Memory Encryption, ROP Attack Prevention and Advanced Crypto-Key Protection
- option for Sanitization Utility Software Package
- option for proprietary board-level security

Software Support

- supports Linux® and Windows®
- for other operating systems such as VxWorks®, contact Concurrent Technologies for further information

Firmware Support

- dual 32 Mbyte BIOS SPI Flash EPROMs
- UEFI boot firmware (BIOS):
 - → UEFI 2.7 support
 - → implements Secure Boot
- implements Intel Boot Guard
- optional Fast Boot solution using the Intel Firmware Support Package (FSP)
- LAN boot firmware included

Safety

■ PCB (PWB) manufactured with flammability rating of UL94V-0

Electrical Specification (Estimated)

- typical current figure for Intel Tiger Lake-H Processor with 32 Gbytes DRAM:
 - → +12 V VS1 @ 3.5 A
 - → +3.3 V AUX @ 0.4 A
- +12 V AUX and -12 V AUX routed to XMC site
- +5 V and +3.3 V are not connected

Environmental Specification

- Air flow thru cooled (VITA 48.8)
- operating temperature at air inlet:
 - → VITA 47.1 Class FC2, -40°C to +55°C
- non-operating temperature:
 - → VITA 47 Class C4, -55°C to +105°C
- relative humidity: 5% to 95%, non-condensing

Mechanical Specification

- 3U VPX form-factor (VITA 46.0, VITA 48.0)
- 3.9-inches x 6.3-inches (100 mm x 160 mm)
- slot width (VITA 48.8):
 - → 1.5-inch Type 1 Two Level Maintenance
- connectors to VITA 46.0 for P0, P1 and P2
- operating mechanical:
 - → shock VITA 47 Class OS2, 40 g
 - → random vibration VITA 47 Class V3, 0.1 g²/Hz